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## 1 [Fast detection of communication patterns in distributed executions](#)

Thomas Kunz, Michiel F. H. Seuren

November 1997 **Proceedings of the 1997 conference of the Centre for Advanced Studies on Collaborative research**

Full text available: ☒ pdf(4.21 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Understanding distributed applications is a tedious and difficult task. Visualizations based on process-time diagrams are often used to obtain a better understanding of the execution of the application. The visualization tool we use is Poet, an event tracer developed at the University of Waterloo. However, these diagrams are often very complex and do not provide the user with the desired overview of the application. In our experience, such tools display repeated occurrences of non-trivial commun ...

## 2 [Special issue: AI in engineering](#)

D. Sriram, R. Joobbani

January 1985 **ACM SIGART Bulletin**, Issue 91

Full text available: ☒ pdf(8.79 MB)

Additional Information: [full citation](#), [abstract](#)

The papers in this special issue were compiled from responses to the announcement in the July 1984 issue of the SIGART newsletter and notices posted over the ARPAnet. The interest being shown in this area is reflected in the sixty papers received from over six countries. About half the papers were received over the computer network.

## 3 [System-level power optimization: techniques and tools](#)

Luca Benini, Giovanni de Micheli

April 2000 **ACM Transactions on Design Automation of Electronic Systems (TODAES)**, Volume 5 Issue 2

Full text available: ☒ pdf(385.22 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

This tutorial surveys design methods for energy-efficient system-level design. We consider electronic systems consisting of a hardware platform and software layers. We consider the three major constituents of hardware that consume energy, namely computation, communication, and storage units, and we review methods of reducing their energy consumption. We also study models for analyzing the energy cost of software, and methods for energy-efficient software design and compilation. This survey ...

## 4 [Special issue: Game-playing programs: theory and practice](#)

M. A. Bramer

April 1972 **ACM SIGART Bulletin**, Issue 80


Full text available: ☒ pdf(9.23 MB)

Additional Information: [full citation](#), [abstract](#)

This collection of articles has been brought together to provide SIGART members with an overview of Artificial Intelligence approaches to constructing game-playing programs. Papers on both theory and practice are included.

## 5 [Pen computing: a technology overview and a vision](#)

André Meyer

Full text available:  [pdf\(5.14 MB\)](#)


Additional Information: [full citation](#), [abstract](#), [citations](#), [index terms](#)

This work gives an overview of a new technology that is attracting growing interest in public as well as in the computer industry itself. The visible difference from other technologies is in the use of a pen or pencil as the primary means of interaction between a user and a machine, picking up the familiar pen and paper interface metaphor. From this follows a set of consequences that will be analyzed and put into context with other emerging technologies and visions. Starting with a short historic ...

6 The family of concurrent logic programming languages

Ehud Shapiro

September 1989 **ACM Computing Surveys (CSUR)**, Volume 21 Issue 3

Full text available:  [pdf\(9.62 MB\)](#)


Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Concurrent logic languages are high-level programming languages for parallel and distributed systems that offer a wide range of both known and novel concurrent programming techniques. Being logic programming languages, they preserve many advantages of the abstract logic programming model, including the logical reading of programs and computations, the convenience of representing data structures with logical terms and manipulating them using unification, and the amenability to metaprogrammin ...

7 Special issue on knowledge representation

Ronald J. Brachman, Brian C. Smith

February 1980 **ACM SIGART Bulletin**, Issue 70

Full text available:  [pdf\(13.13 MB\)](#)

Additional Information: [full citation](#), [abstract](#)

In the fall of 1978 we decided to produce a special issue of the SIGART Newsletter devoted to a survey of current knowledge representation research. We felt that there were two useful functions such an issue could serve. First, we hoped to elicit a clear picture of how people working in this subdiscipline understand knowledge representation research, to illuminate the issues on which current research is focused, and to catalogue what approaches and techniques are currently being developed. Secon ...

8 Garbage collecting the Internet: a survey of distributed garbage collection

Saleh E. Abdullahi, Graem A. Ringwood

September 1998 **ACM Computing Surveys (CSUR)**, Volume 30 Issue 3

Full text available:  [pdf\(337.65 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

Internet programming languages such as Java present new challenges to garbage-collection design. The spectrum of garbage-collection schema for linked structures distributed over a network are reviewed here. Distributed garbage collectors are classified first because they evolved from single-address-space collectors. This taxonomy is used as a framework to explore distribution issues: locality of action, communication overhead and indeterministic communication latency.


**Keywords:** automatic storage reclamation, distributed, distributed file systems, distributed memories, distributed object-oriented management, memory management, network communication, object-oriented databases, reference counting

9 The Wisconsin Wind Tunnel: virtual prototyping of parallel computers

Steven K. Reinhardt, Mark D. Hill, James R. Larus, Alvin R. Lebeck, James C. Lewis, David A. Wood


June 1993 **ACM SIGMETRICS Performance Evaluation Review , Proceedings of the 1993 ACM SIGMETRICS conference on Measurement and modeling of computer systems**, Volume

21 Issue 1

Full text available:  [pdf\(1.40 MB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

We have developed a new technique for evaluating cache coherent, shared-memory computers. The Wisconsin Wind Tunnel (WWT) runs a parallel shared-memory program on a parallel computer (CM-5) and uses execution-driven, distributed, discrete-event simulation to accurately calculate program execution time. WWT is a virtual prototype that exploits similarities between the system under design (the target) and an existing evaluation platform (the host). The host directly executes all target program ins ...

Full text available:  [pdf\(3.74 MB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Fast, efficient logic simulators are an essential tool in modern VLSI system design. Logic simulation is used extensively for design verification prior to fabrication, and as VLSI systems grow in size, the execution time required by simulation is becoming more and more significant. Faster logic simulators will have an appreciable economic impact, speeding time to market while ensuring more thorough system design testing. One approach to this problem is to utilize parallel processing, taking ...

**Keywords:** circuit structure, parallel architecture, parallelism, partitioning, synchronization algorithm, timing granularity

11 Piranha: a scalable architecture based on single-chip multiprocessing

Luiz André Barroso, Kourosh Gharachorloo, Robert McNamara, Andreas Nowatzky, Shaz Qadeer, Barton Sano, Scott Smith, Robert Stets, Ben Verghese

May 2000 **ACM SIGARCH Computer Architecture News , Proceedings of the 27th annual international symposium on Computer architecture**, Volume 28 Issue 2

Full text available:  [pdf\(191.10 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

The microprocessor industry is currently struggling with higher development costs and longer design times that arise from exceedingly complex processors that are pushing the limits of instruction-level parallelism. Meanwhile, such designs are especially ill suited for important commercial applications, such as on-line transaction processing (OLTP), which suffer from large memory stall times and exhibit little instruction-level parallelism. Given that commercial applications constitute by fa ...

12 Continuous profiling: where have all the cycles gone?

Jennifer M. Anderson, Lance M. Berc, Jeffrey Dean, Sanjay Ghemawat, Monika R. Henzinger, Shun-Tak A. Leung, Richard L. Sites, Mark T. Vandevoorde, Carl A. Waldspurger, William E. Weihl

November 1997 **ACM Transactions on Computer Systems (TOCS)**, Volume 15 Issue 4

Full text available:  [pdf\(259.35 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

This article describes the Digital Continuous Profiling Infrastructure, a sampling-based profiling system designed to run continuously on production systems. The system supports multiprocessors, works on unmodified executables, and collects profiles for entire systems, including user programs, shared libraries, and the operating system kernel. Samples are collected at a high rate (over 5200 samples/sec. per 333MHz processor), yet with low overhead (1-3% slowdown for most workloads). A ...

**Keywords:** performance understanding, performance-monitoring hardware, profiling, program analysis

13 Fast hardware-software co-simulation using VHDL models

Bassam Tabbara, Enrica Filippi, Luciano Lavagno

January 1999 **Proceedings of the conference on Design, automation and test in Europe**


Full text available:  [pdf\(548.81 KB\)](#)

Additional Information: [full citation](#), [citations](#), [index terms](#)

14 The Jalapeño dynamic optimizing compiler for Java

Michael G. Burke, Jong-Deok Choi, Stephen Fink, David Grove, Michael Hind, Vivek Sarkar, Mauricio J. Serrano, V. C. Sreedhar, Harini Srinivasan, John Whaley

June 1999 **Proceedings of the ACM 1999 conference on Java Grande**


Full text available:  [pdf\(1.34 MB\)](#)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

15 Continuous profiling: where have all the cycles gone?

Jennifer M. Anderson, Lance M. Berc, Jeffrey Dean, Sanjay Ghemawat, Monika R. Henzinger, Shun-Tak A. Leung, Richard L. Sites, Mark T. Vandevoorde, Carl A. Waldspurger, William E. Weihl

October 1997 **ACM SIGOPS Operating Systems Review , Proceedings of the sixteenth ACM symposium on Operating systems principles**, Volume 31 Issue 5

Full text available:  [pdf\(2.29 MB\)](#)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

16 Elastic time

Sudhir Srinivasan, Paul F. Reynolds

April 1998 **ACM Transactions on Modeling and Computer Simulation (TOMACS)**, Volume 8 Issue 2

Full text available:  [pdf\(582.40 KB\)](#)


Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

We introduce a new class of synchronization protocols for parallel discrete event simulation, those based on near-perfect state information (NPSI). NPSI protocols are adaptive dynamically controlling the rate at which processes constituting a parallel simulation proceed with the goal of completing a simulation efficiently. We show by analysis that a class of adaptive protocols (that includes NPSI and several others) can both arbitrarily outperform and be arbitrarily outperformed ...

**Keywords:** adaptive protocols, aggressiveness, near-perfect state information, optimistic protocols, risk

17 Summary of ACM/ONR workshop on parallel and distributed debugging

January 1992 **ACM SIGOPS Operating Systems Review**, Volume 26 Issue 1


Full text available:  [pdf\(1.31 MB\)](#)

Additional Information: [full citation](#), [citations](#), [index terms](#)

18 A unified distributed simulation system

Jeff McAffer

December 1990 **Proceedings of the 22nd conference on Winter simulation**


Full text available:  [pdf\(1.01 MB\)](#)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

19 Planar-adaptive routing: low-cost adaptive networks for multiprocessors

Andrew A. Chien, Jae H. Kim

January 1995 **Journal of the ACM (JACM)**, Volume 42 Issue 1

Full text available:  [pdf\(2.28 MB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)


Network throughput can be increased by allowing multipath, adaptive routing. Adaptive routing allows more freedom in the paths taken by messages, spreading load over physical channels more evenly. The flexibility of adaptive routing introduces new possibilities of deadlock. Previous deadlock avoidance schemes in k-ary n-cubes require an exponential number of virtual channels. We describe a family of deadlock-free routing algorithms, called planar-ad ...

**Keywords:** adaptive routing, fault tolerance, interconnection networks, multicomputers, packet routing, parallel processing, transmission-order preservation

20 Effects of communication latency, overhead, and bandwidth in a cluster architecture

Richard P. Martin, Amin M. Vahdat, David E. Culler, Thomas E. Anderson

May 1997 **ACM SIGARCH Computer Architecture News , Proceedings of the 24th annual international symposium on Computer architecture**, Volume 25 Issue 2

Full text available:  [pdf\(2.08 MB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

This work provides a systematic study of the impact of communication performance on parallel applications in a high performance network of workstations. We develop an experimental system in which the communication latency, overhead, and bandwidth can be independently varied to observe the effects on a wide range of applications. Our results indicate that current efforts to improve cluster communication performance to that of tightly integrated parallel machines results in significantly improved ...

**Inventor Name Search Result**

Your Search was:

Last Name = BORTFELD

First Name = ULRICH

| Application#             | Patent#                 | Status | Date Filed | Title   | Inventor Name    |
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| <a href="#">11144480</a> | Not Issued              | 020    | 06/02/2005 | METHOD AND APPARATUS FOR ACCELERATED SIMULATION OF ELECTRONIC SYSTEM DESIGNS                  | BORTFELD, ULRICH |
| <a href="#">10816144</a> | Not Issued              | 030    | 03/31/2004 | INSTRUCTION-WORD ADDRESSABLE L0 INSTRUCTION CACHE   | BORTFELD, ULRICH |
| <a href="#">10153576</a> | Not Issued              | 041    | 05/21/2002 | METHOD AND SYSTEM FOR DEBUGGING MULTIPROCESSOR SYSTEMS  | BORTFELD, ULRICH |
| <a href="#">09586433</a> | Not Issued              | 092    | 06/02/2000 | METHOD AND APPARATUS FOR UNIFIED SIMULATION   | BORTFELD, ULRICH |
| <a href="#">09586325</a> | Not Issued              | 071    | 06/02/2000 | METHOD AND APPARATUS FOR ACCELERATING HARDWARE SIMULATION                                     | BORTFELD, ULRICH |
| <a href="#">09571924</a> | <a href="#">6718294</a> | 150    | 05/16/2000 | SYSTEM AND METHOD FOR SYNCHRONIZED CONTROL OF SYSTEM SIMULATORS WITH MULTIPLE PROCESSOR CORES | BORTFELD, ULRICH |

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Last Name = ANDERSSON

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| <a href="#">60689819</a> | Not Issued              | 020    | 06/13/2005 | METHOD AND SYSTEM FOR AFFINITY ANALYSIS   | ANDERSSON, KARL      |
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| <a href="#">29133712</a> | <a href="#">D451285</a> | 150    | 12/05/2000 | TOOTHBRUSH  | ANDERSSON, KARL-AXEL |
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| <u>09087402</u> | <u>6289286</u> | 150 | 05/29/1998 | SURFACE REGENERATION OF<br>BIOSENSORS AND<br>CHARACTERIZATION OF<br>BIOMOLECULES ASSOCIATED<br>THEREWITH                               | ANDERSSON, KARL              |
| <u>07966804</u> | <u>5293654</u> | 250 | 10/26/1992 | ENERGY AND WATER SAVING<br>SHOWER ASSEMBLY   | ANDERSSON, KARL F.           |
| <u>07934535</u> | <u>5360502</u> | 150 | 10/06/1992 | METHOD AND APPARATUS FOR<br>SPLICING AT LEAST TWO SINGLE<br>OR MULTIPLE LAYER MATERIALS<br>COMPRISING AIR PERMEABLE SOFT<br>PAPER WEBS | ANDERSSON, KARL G.<br>B.     |
| <u>07116333</u> | <u>D310974</u> | 150 | 11/03/1987 | WHEEL CAMBER METER<br>MOUNTING   | ANDERSSON, KARL E.<br>P.     |
| <u>06729136</u> | <u>D293064</u> | 150 | 04/30/1985 | TURNER   | ANDERSSON, KARL A.           |
| <u>06729135</u> | <u>D293063</u> | 150 | 04/30/1985 | SMALL TURNER   | ANDERSSON, KARL A.           |
| <u>06729134</u> | <u>D293065</u> | 150 | 04/30/1985 | STIRRING LADLE WITH SLITS  | ANDERSSON, KARL A.           |
| <u>06729133</u> | <u>D294323</u> | 150 | 04/30/1985 | LADLE  | ANDERSSON, KARL A.           |
| <u>06729040</u> | <u>D293066</u> | 150 | 04/30/1985 | SKIMMER  | ANDERSSON, KARL A.           |
| <u>06729037</u> | <u>D292959</u> | 150 | 04/30/1985 | WHISK  | ANDERSSON, KARL A.           |
| <u>06711004</u> | <u>4641464</u> | 150 | 03/12/1985 | DOWNPIPE   | ANDERSSON, KARL G.           |
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| <u>06675083</u> | <u>4655710</u> | 150 | 11/26/1984 | METHOD FOR REPLACING LOST<br>DENTAL SUBSTANCE  | ANDERSSON, KARL E.<br>MORGAN |
| <u>06621605</u> | <u>D285520</u> | 150 | 06/18/1984 | VACUUM JUG   | ANDERSSON, KARL A.           |

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| <u>06527475</u> | <u>4517879</u> | 150 | 08/29/1983 | MACHINE COMPRISING A RECIPROCATING OPERATIVE MEMBER   | ANDERSSON, KARL ERIK E.      |
| <u>06503154</u> | <u>4513579</u> | 150 | 05/16/1983 | METHOD AND ARRANGEMENT FOR MAINTAINING A FROST-FREE FREEZER   | ANDERSSON, KARL F.           |
| <u>06501817</u> | <u>D280454</u> | 150 | 06/07/1983 | DISH DRAINER  | ANDERSSON, KARL A.           |
| <u>06435042</u> | Not Issued     | 161 | 10/18/1982 | ASSEMBLY FOR MAINTAINING A FREEZER FROST-FREE   | ANDERSSON, KARL F.           |
| <u>06426107</u> | <u>4526653</u> | 150 | 09/28/1982 | CONTROL SYSTEM FOR A MULTILAYER HEADBOX USING AN ULTRASONIC   | ANDERSSON, KARL J. L.        |
| <u>06372654</u> | Not Issued     | 161 | 04/28/1982 | METHOD OF MANUFACTURING A FIBRE PRODUCT   | ANDERSSON, KARL G. B.        |
| <u>06346869</u> | <u>4435483</u> | 250 | 02/08/1982 | LOOSE SINTERING OF SPHERICAL FERRITIC-AUSTENITIC STAINLESS STEEL POWDER AND POROUS BODY   | ANDERSSON, KARL H T          |
| <u>06268082</u> | <u>4438163</u> | 150 | 05/28/1981 | PAD WITH SHAPE ADAPTING PROPERTIES  | ANDERSSON, KARL G. B.        |
| <u>06211135</u> | Not Issued     | 161 | 11/28/1980 | ASSEMBLY FOR MAINTAINING A FREEZER FROST-FREE   | ANDERSSON, KARL F.           |
| <u>06196556</u> | <u>4380083</u> | 150 | 05/21/1980 | METHOD OF AND AN ARRANGEMENT IN A TELECOMMUNICATION SYSTEM FOR REGULATING THE PHASE POSITION OF A CONTROLLED SIGNAL IN RELATION TO A REFERENCE SIGNAL | ANDERSSON, KARL A. I.        |
| <u>06021065</u> | <u>4240482</u> | 150 | 03/16/1979 | BAG-LIKE LIQUID CONTAINERS, ESPECIALLY FOR MEDICAL TREATMENT  | ANDERSSON, KARL GOSTA BERTIL |
| <u>06016345</u> | <u>4261057</u> | 150 | 02/28/1979 | DISPOSABLE BIB AND A METHOD FOR ITS MANUFACTURE   | ANDERSSON, KARL GOSTA BERTIL |
| <u>05839312</u> | <u>4289253</u> | 150 | 10/04/1977 | METHOD AND A DEVICE FOR WARMING AND DAMPING OF NON-WOVEN, DISPOSABLE, WET STRENGTH TREATED NAPKINS OR TOWELS  | ANDERSSON, KARL GOSTA BERTIL |

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